

FIG. 1

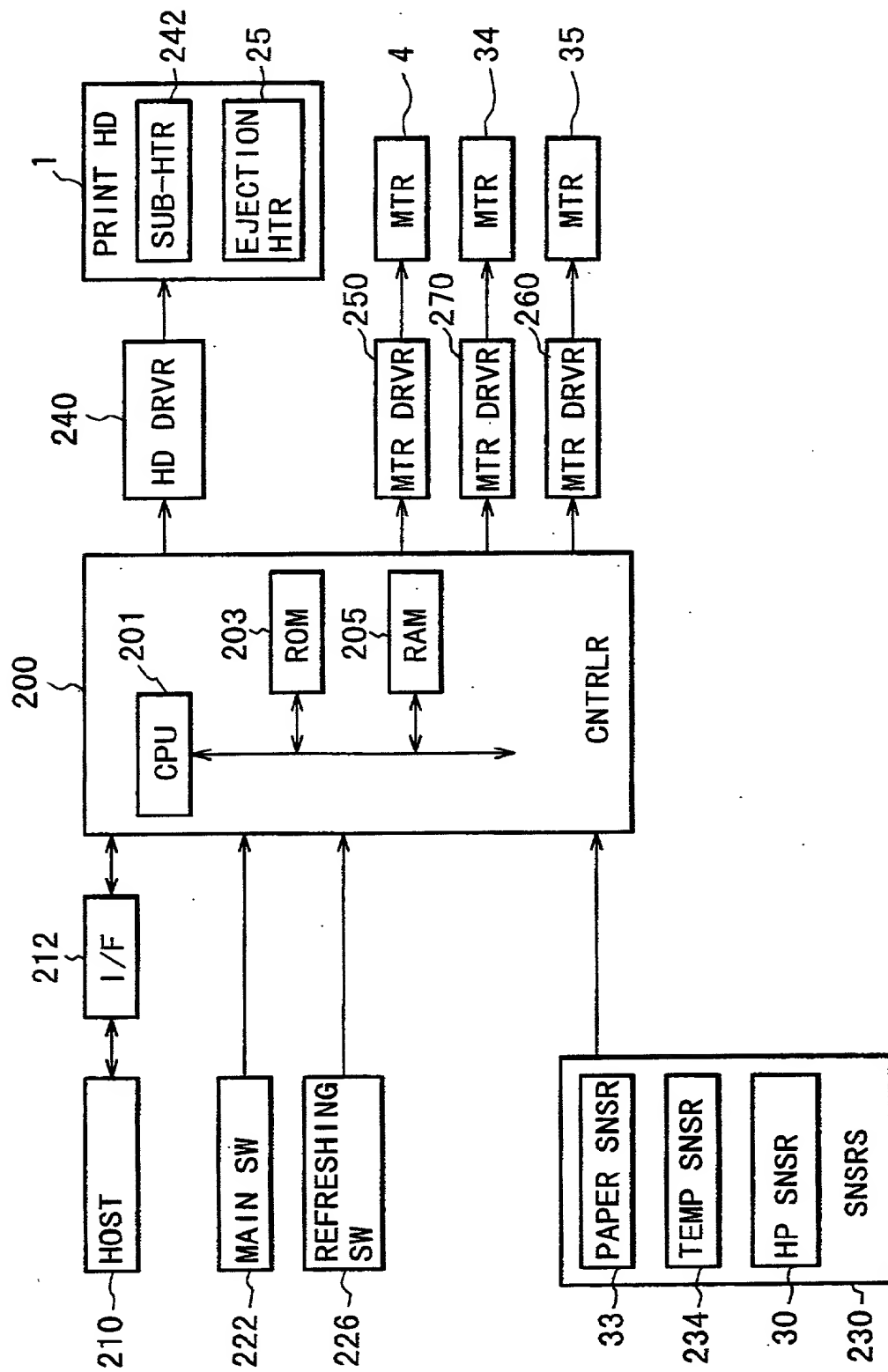


FIG. 2

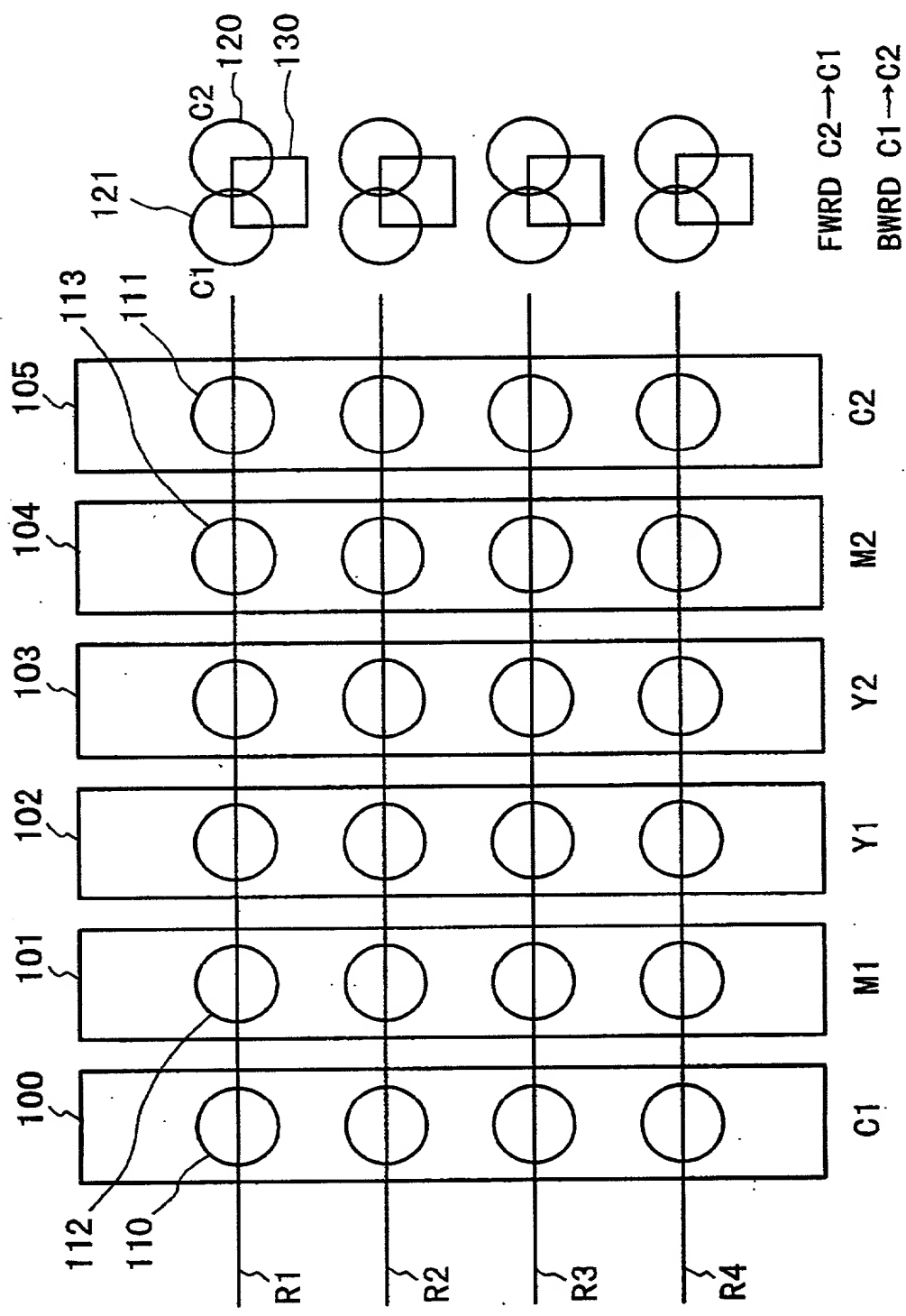


FIG. 3

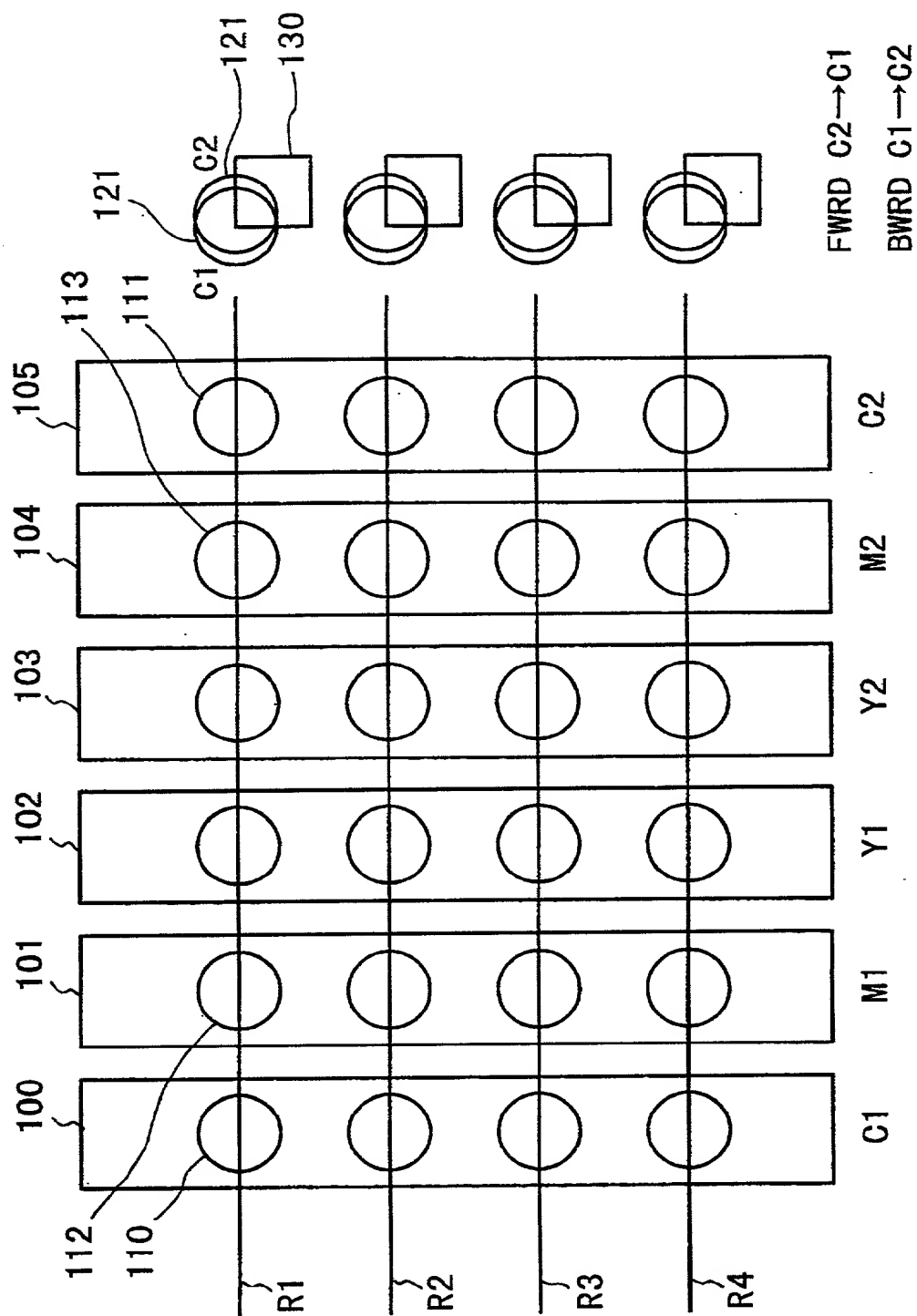


FIG. 4

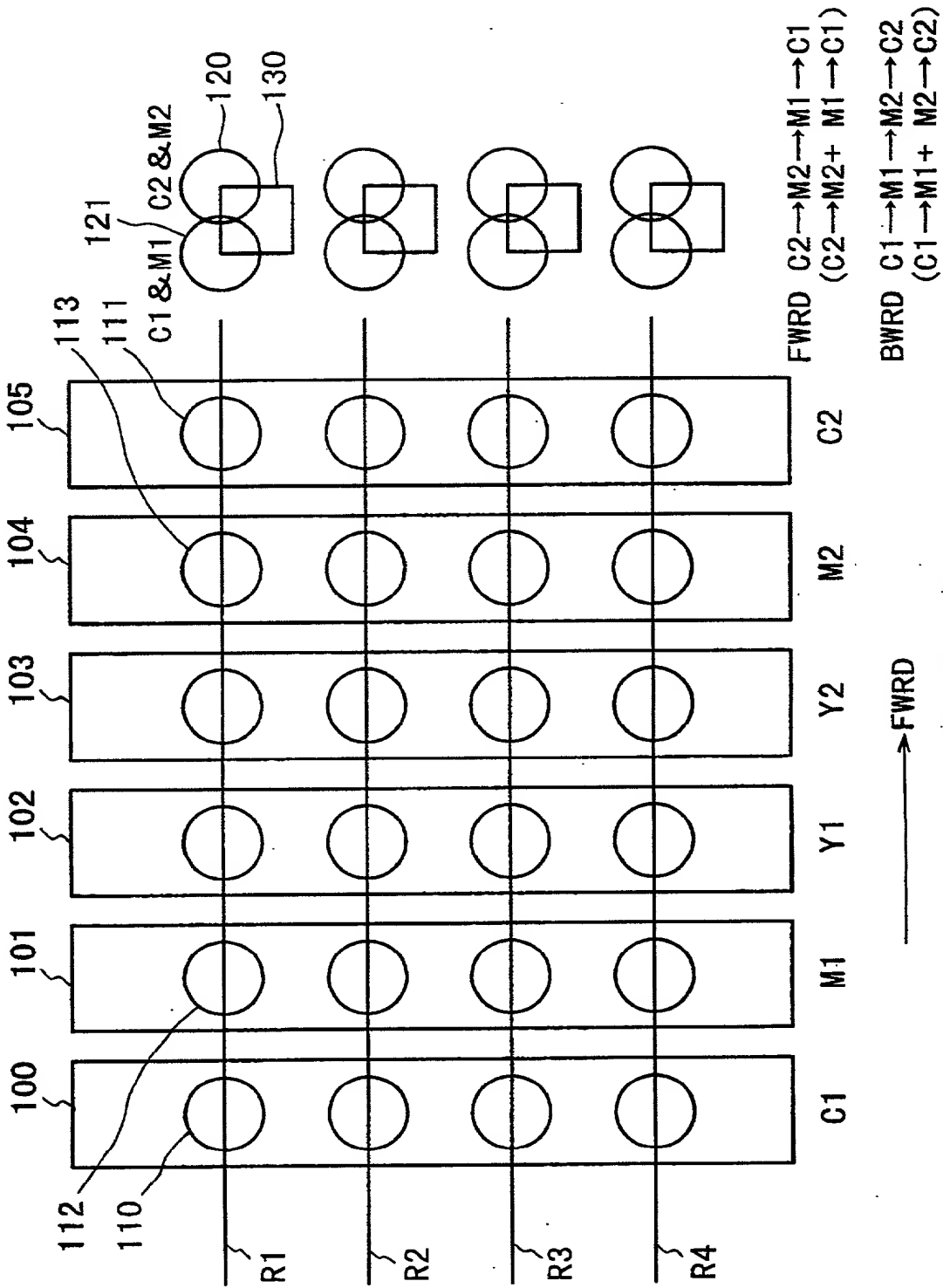


FIG. 5

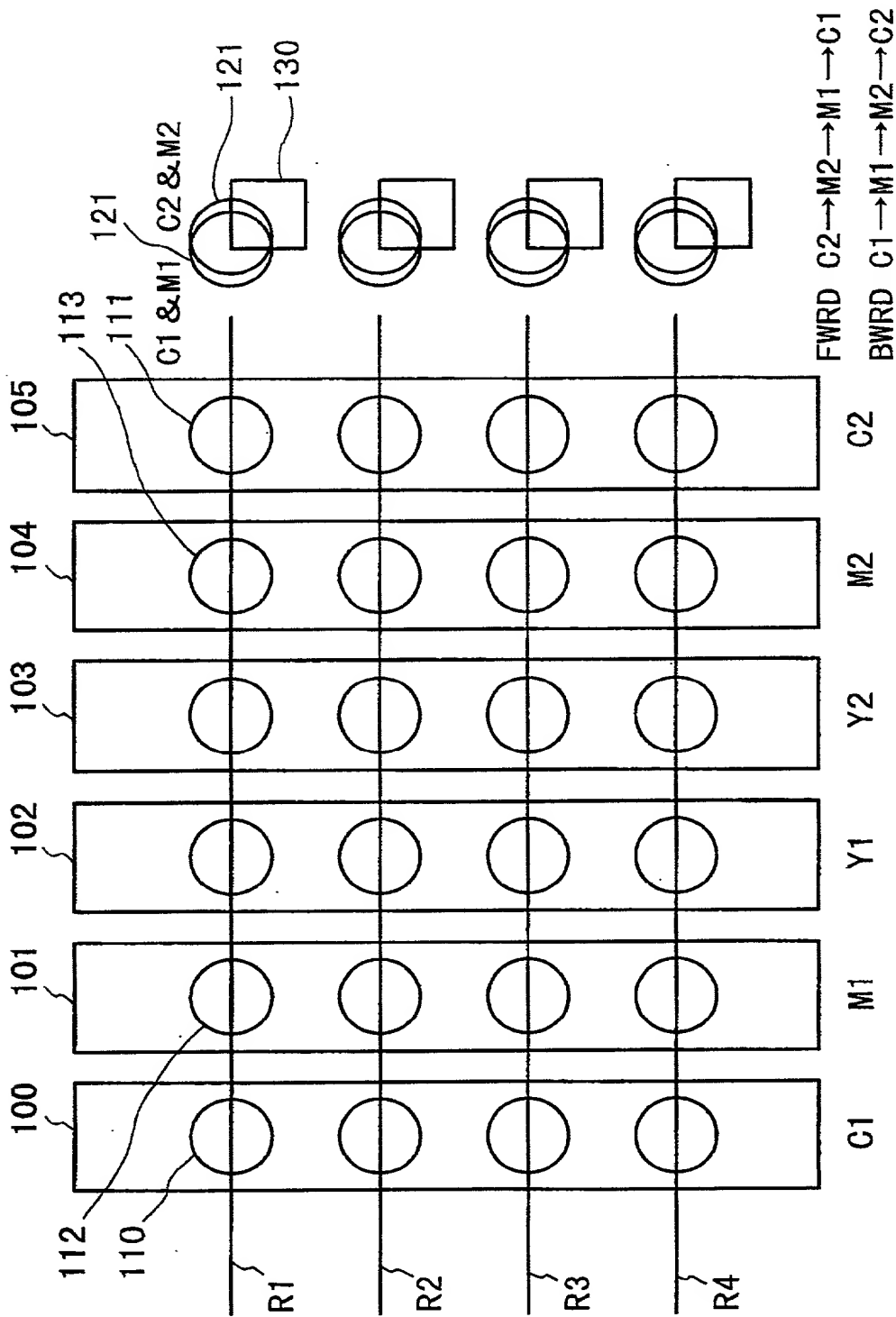


FIG. 6

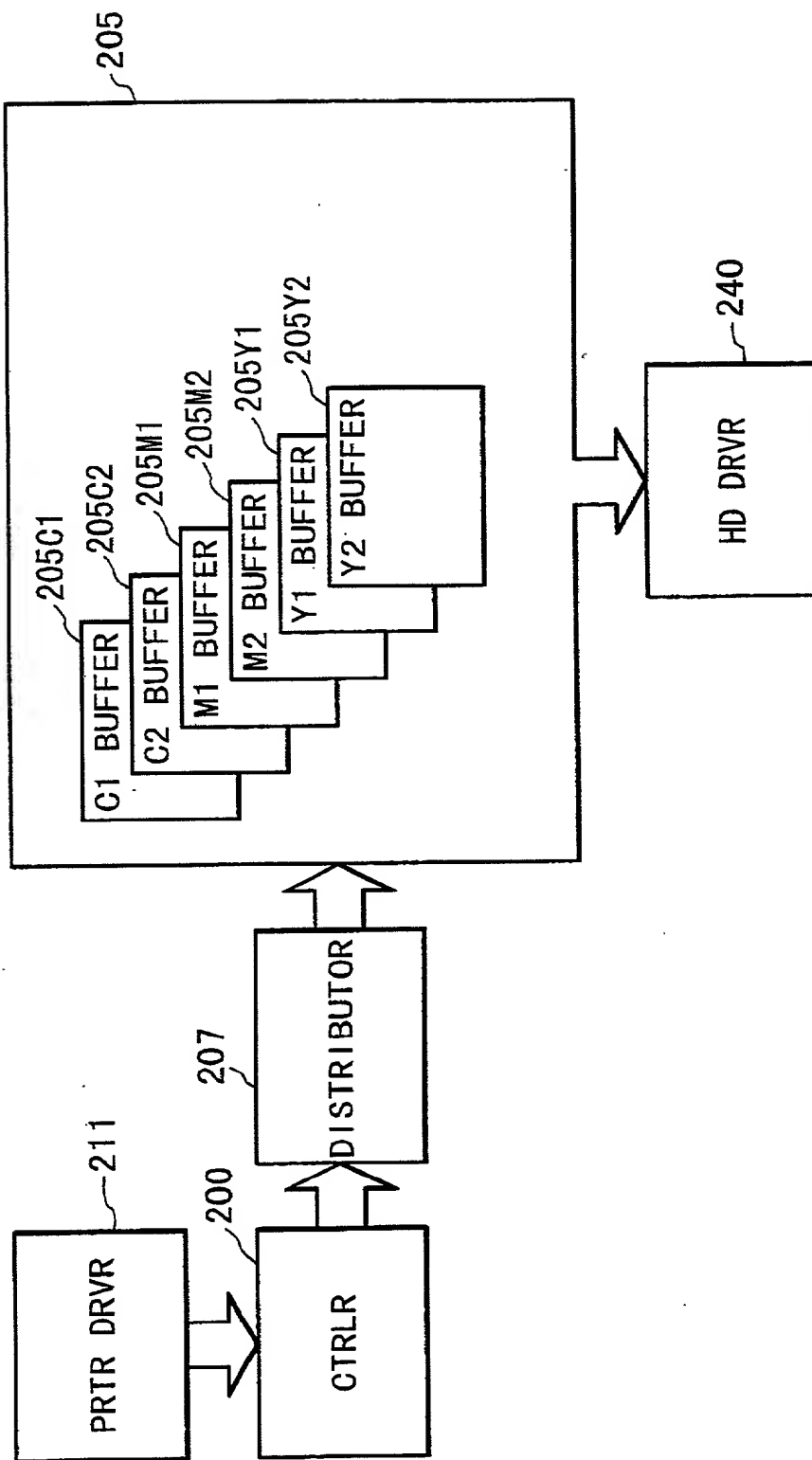


FIG. 7

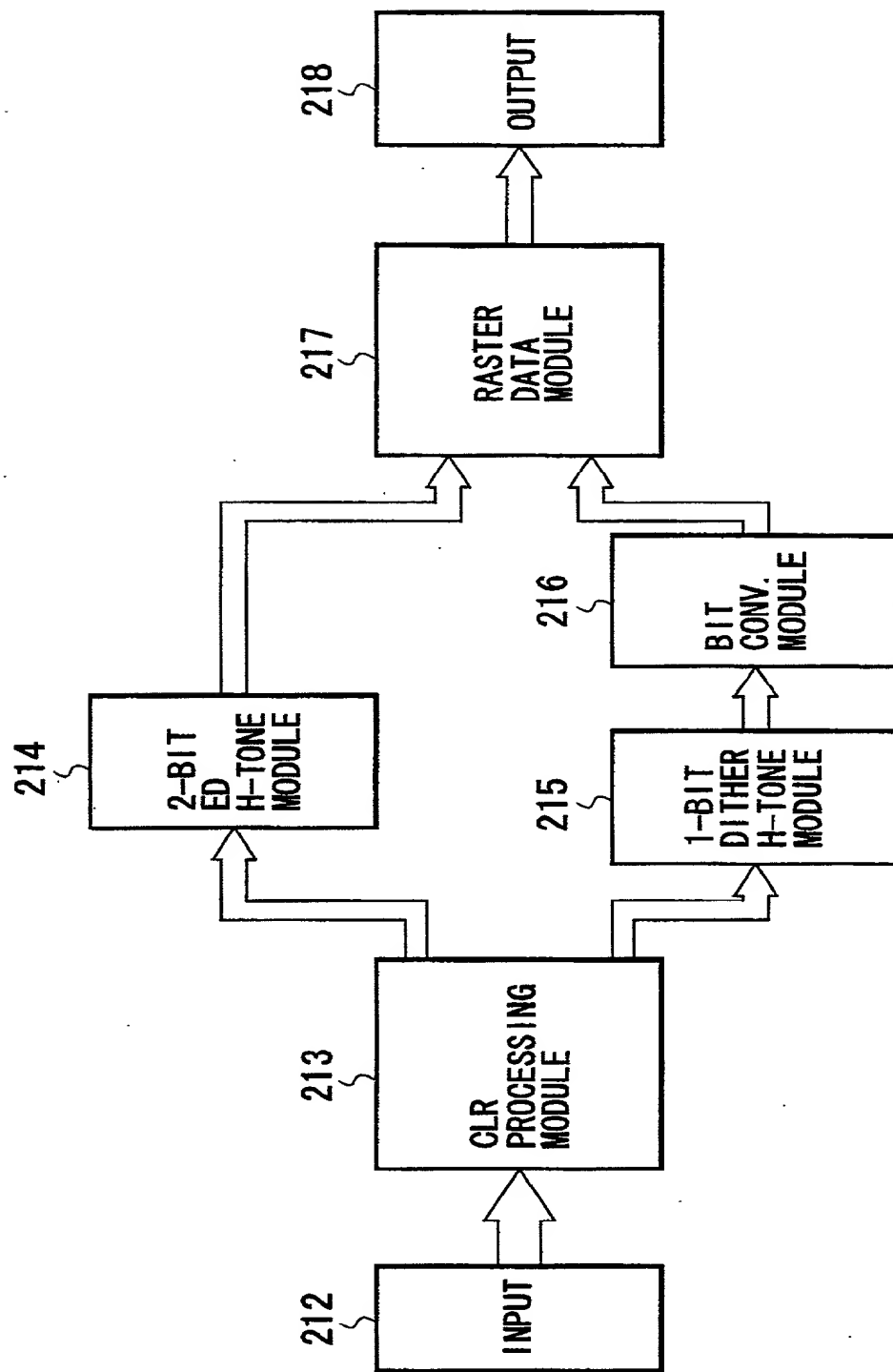


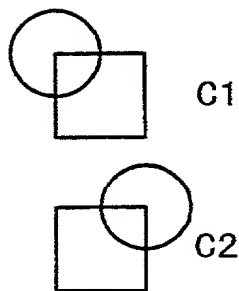
FIG. 8

INPUT DATA 00

(a) c



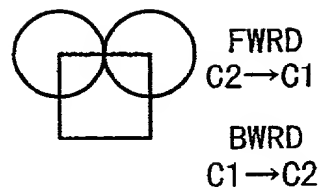
01



C1

C2

10



FWRD
C2→C1

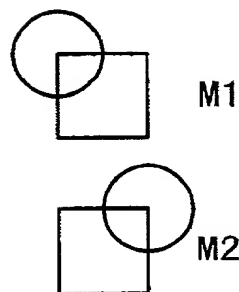
BWRD
C1 → C2

INPUT DATA 00

(b) M



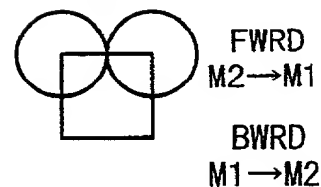
01



M1

M2

10



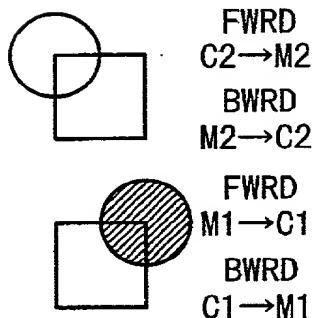
FWRD
M2→M1

BWRD
M1 → M2

INPUT DATA 00

(c) Blue

01



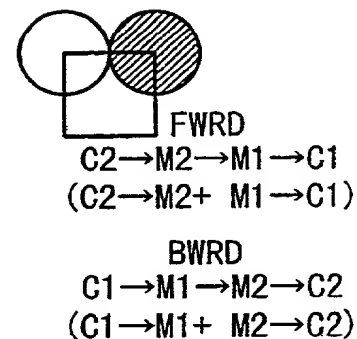
FWRD
C2→M2

BWRD
M2→C2

FWRD
M1 → C1

BWRD
C1→M1

10



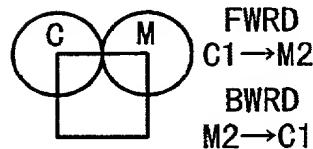
FWRD

$$C2 \rightarrow M2 \rightarrow M1 \rightarrow C1$$

$$(C2 \rightarrow M2 + M1 \rightarrow C1)$$

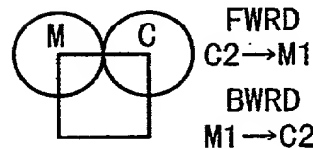
BWRD

$$C1 \rightarrow M1 \rightarrow M2 \rightarrow C2$$

$$(C1 \rightarrow M1 + M2 \rightarrow C2)$$


FWRD
C1 → M2

BWRD
M2→C1



FWRD
C2→M1

BWRD
M1 → C2

FIG. 9

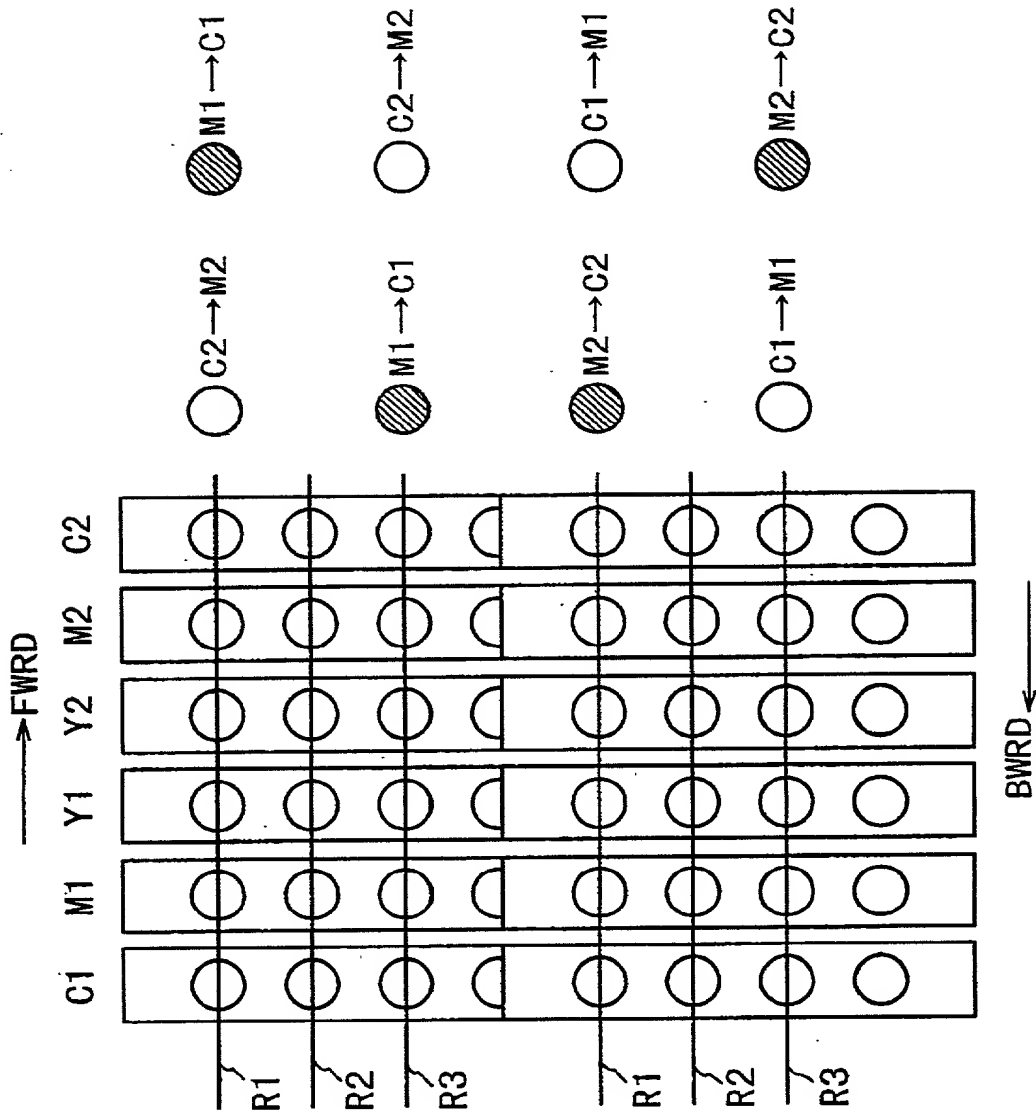


FIG. 10

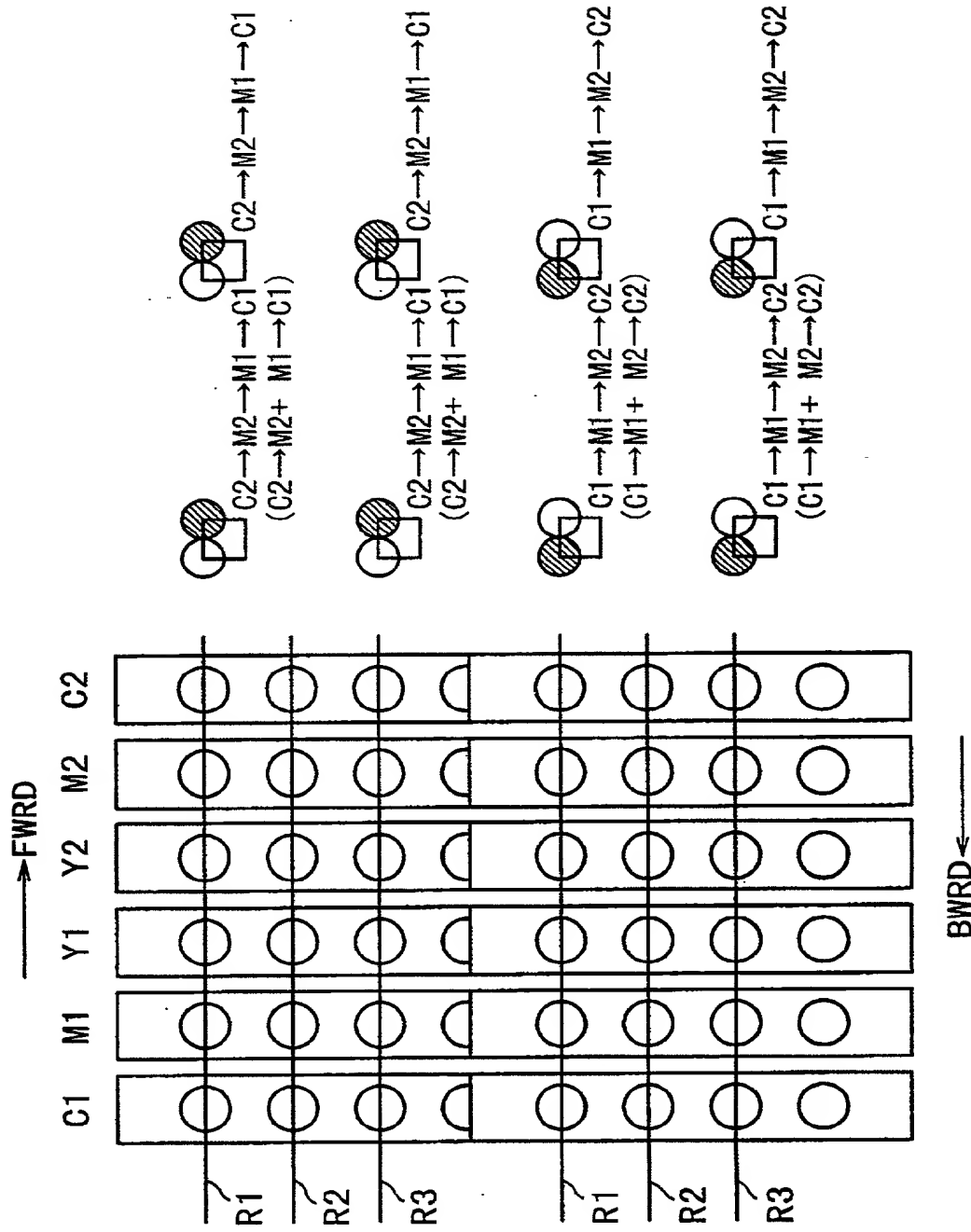


FIG. 11

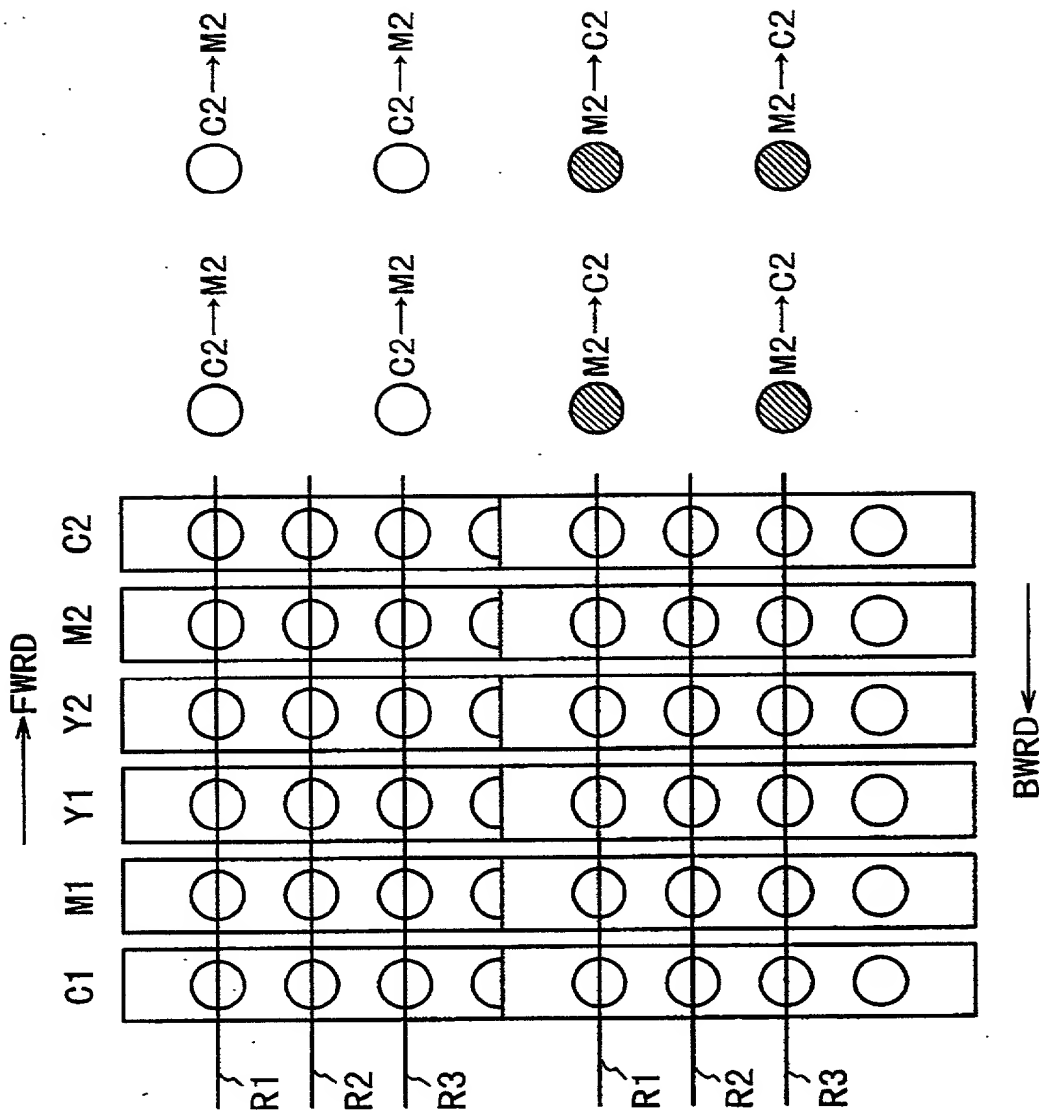


FIG. 12

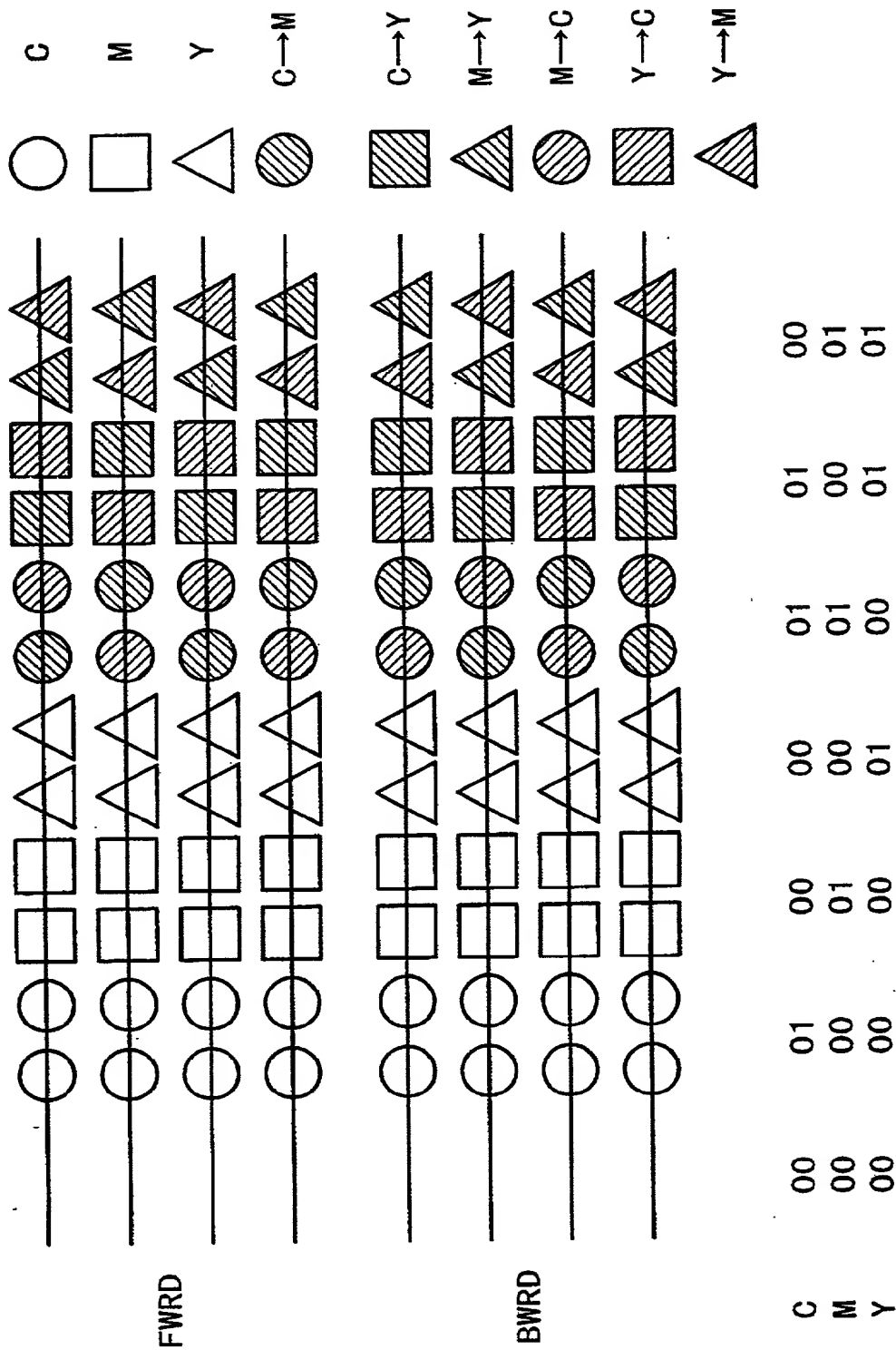


FIG. 13

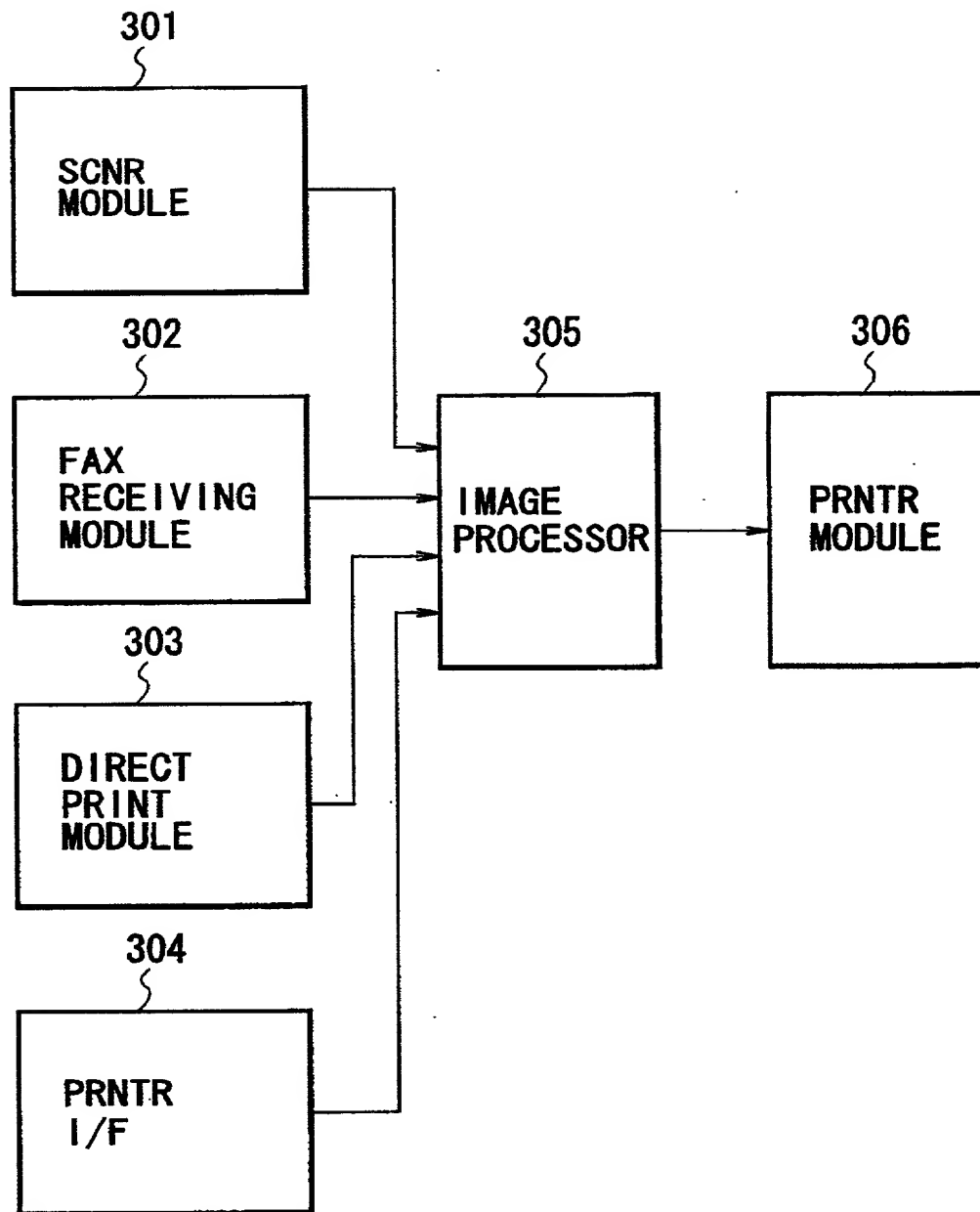


FIG. 14

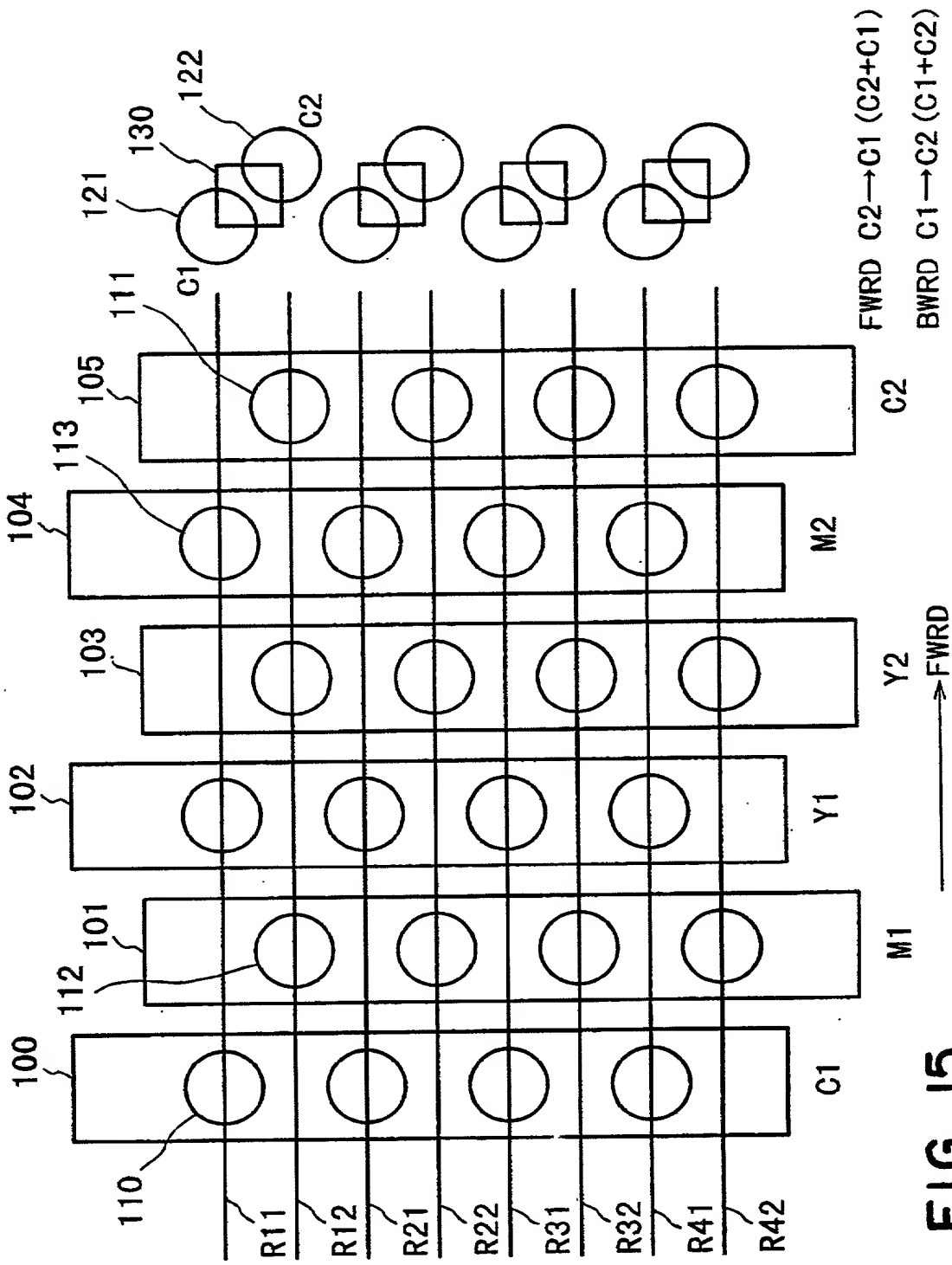


FIG. 15

720dpi

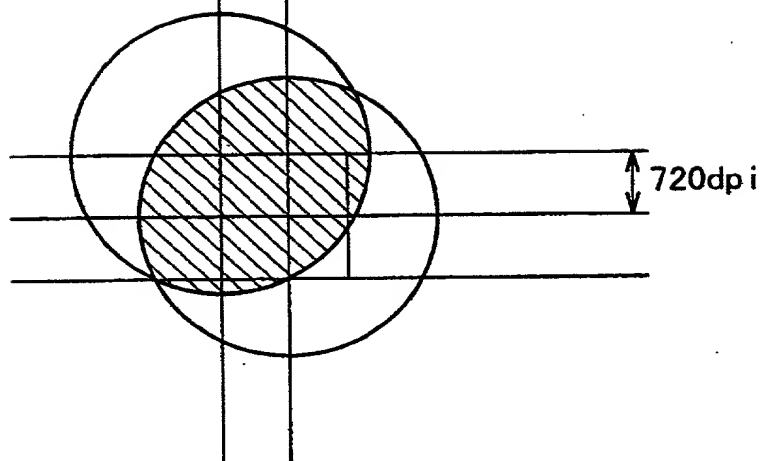
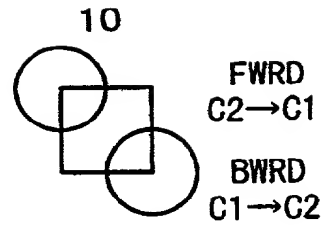
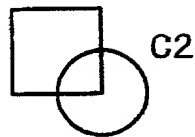
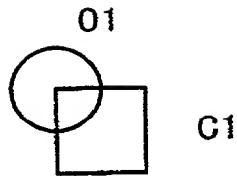
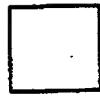


FIG. 16

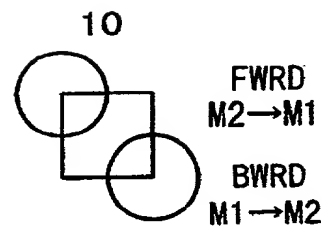
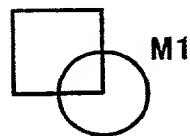
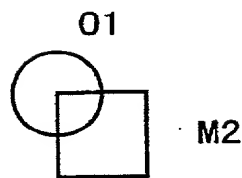
INPUT DATA 00

(a) C



INPUT DATA 00

(b) M



INPUT DATA 00

(c) Blue

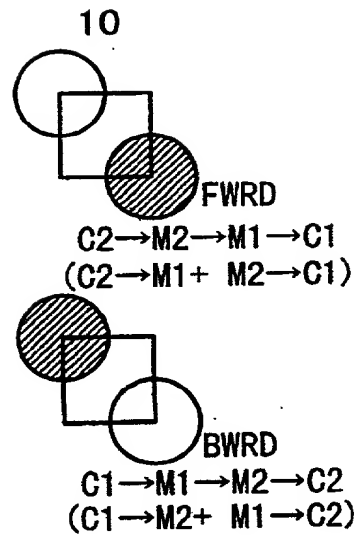
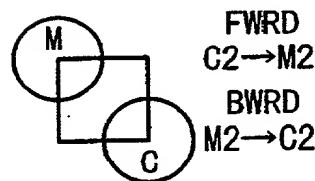
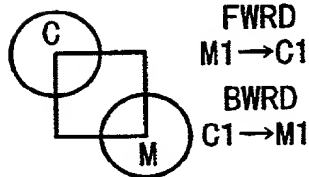
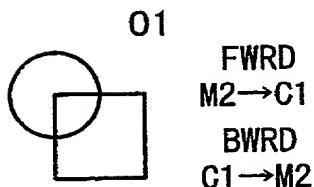


FIG. 17

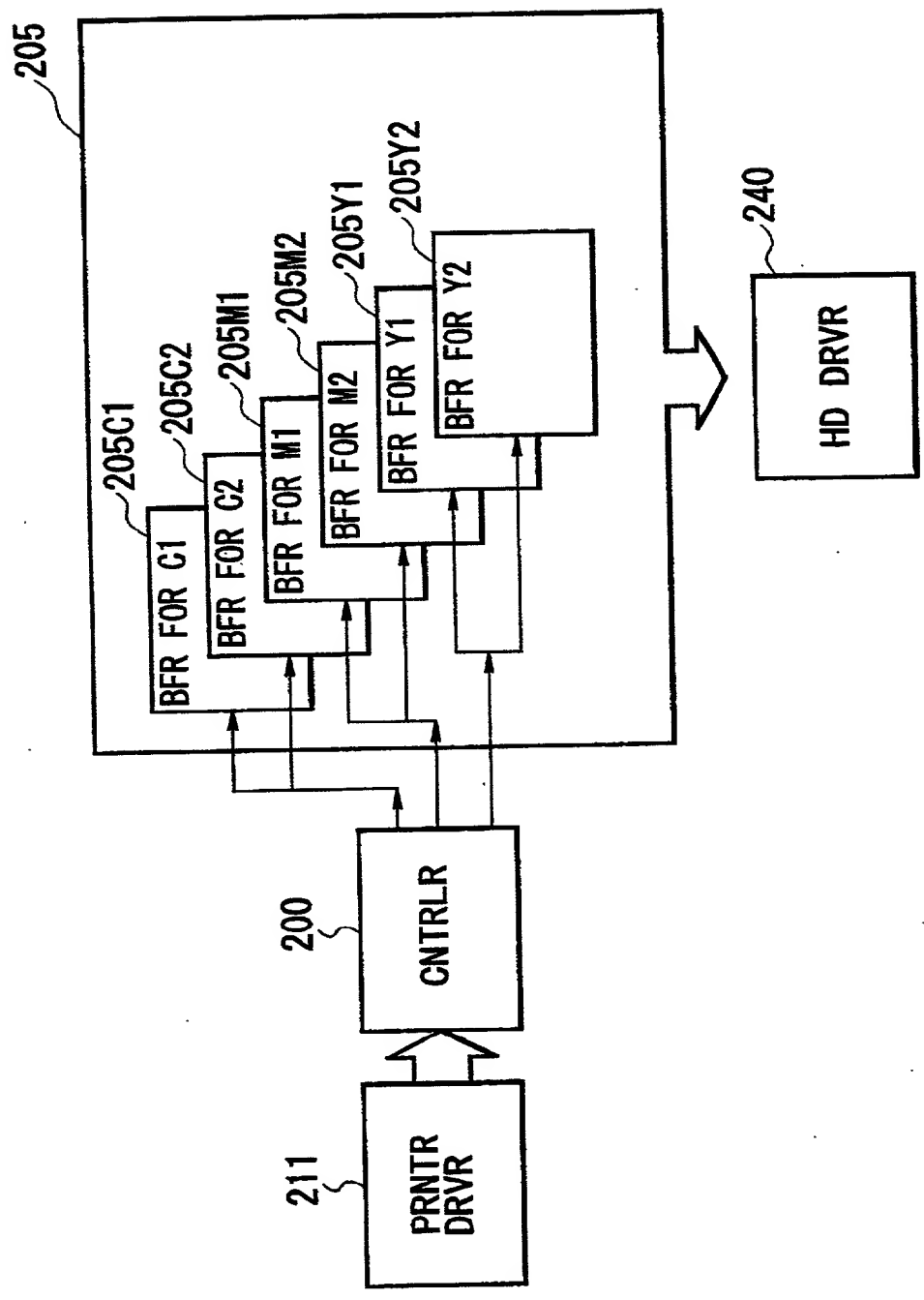


FIG. 18